MHD Shaker Saad				Cor	inputer and commi	iunicati	ion Masters Student
Syrian Beirut, Farabi St						m	nms117@mail.aub.edu Mobile# 76738617
Objectives	_	• •	•		the summer of 2018 to		the opportunity of edge in the industry.
			Education				
Undergraduate School	Computer and cor				2018 with emphasis on Art	ificial in	2014 - 2018 telligence
Graduate School	American University of Beirut						2018 - Present
	Masters with Thes			mina	ation engineering		
Courses Taken	Pattern Recognition ar	DataMinir	DataMining			web Server Design	
Computer skills	Matlab & Simulink		HTML	•	C++/C/C#	_	Java/JavaScript
	ASP.net	•	TensorFlow	•	Python/Django		MySQL
	Professional Experience						
Internship	Proximie, Beirut, Lebano		•				Summer 2017
	Research with my advisor Professor Mazen Saghir on designing a Hardware to accelerate 2D-convolution in neural networks using RISC IV with VHDL						
Research Assistant	Department of Electric Research with my convolution in neu	Extra al and adviso ıral net	curricular Proje Computer Engir r Professor Maz works using RIS	ects neer en S	ing at AUB Saghir on designing a With VHDL	Hardwa	August 2016 are to accelerate 2D-
Research Assistant Final year Project	Department of Electric Research with my convolution in neu Developing a mack We won MSFEA FYP st and we are continuing It is a Minima Small device It allows us to The system ca The devices ca The devices co	Extra al and adviso ural net nine lea artup A it now lly-Inva e that c composite to un be u an cont nnects	curricular Projection Computer Engirer Professor Mazeworks using RIS Carning algorithm Caward. We designated as a startup. Sive IoT Facility an be plugged in the power course.	ects neer S GC IV n to gned Aut n ea onsu angiri eac t has	ring at AUB Saghir on designing a With VHDL enhance branch pred dour FYP as potentia comation System: sily. umption and control ng the building infras ch other. s an Al inside it.	Hardwa diction in lily viable the buil	August 2016 are to accelerate 2D- n processors. e business product
	Department of Electric Research with my convolution in neu Developing a mack We won MSFEA FYP st and we are continuing It is a Minima a Small device It allows us to The system ca The devices co Moreover, we	Extra al and adviso ural net artup A it now lly-Inva e that c compu in be u an coni nnects e create	curricular Projection Computer Engirer Professor Mazeworks using RIST arning algorithm Computer We design as a startup. Sive IoT Facility an be plugged in the power computer the power computer the power computer to a server that and the business	ects neer S GC IV n to Aut n ea onsu angir eac t has mod	ring at AUB Saghir on designing a With VHDL enhance branch pred dour FYP as potentia comation System: sily. umption and control ng the building infras ch other. s an Al inside it.	Hardwa diction in lly viable the buil structure	August 2016 are to accelerate 2D- n processors. e business product lding appliances
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Final year Project	Department of Electric Research with my convolution in neu Developing a mack We won MSFEA FYP st and we are continuing It is a Minima a Small device It allows us to The system ca The devices co Moreover, we Worked on paper	Extra al and adviso ural net nine lea artup A it now illy-Inva e that c compourant be uran cont nnects e create on how Other JBHack urMENA	curricular Projection Computer Engirer Professor Mazeworks using RIS Computer Engirer Professor Mazeworks using RIS Computer Engirer Computer	ects neer sen Sic IV n to Aut n ea onsu angiri c has mod	ring at AUB Saghir on designing a With VHDL enhance branch pred dour FYP as potentia comation System: sily. umption and control ng the building infras ch other. s an AI inside it. del for our FYP. c music using Genera	Hardwa diction in lly viable the buil structure	August 2016 are to accelerate 2D- n processors. e business product lding appliances